

此為範例檔案(部分內容遮蔽)，Calibre 輸出檔案預設檔名為 cellname.lvs.report
上傳檔案時請直接傳送原始純文字格式之檔案，並請保留原始內容

```
#####  
##                               ##  
##          C A L I B R E   S Y S T E M          ##  
##                               ##  
##          L V S   R E P O R T                   ##  
##                               ##  
#####
```

```
REPORT FILE NAME:      circuit.lvs.report  
LAYOUT NAME:          circuit.calibre.db  
SOURCE NAME:          [REDACTED]  
RULE FILE:            [REDACTED]  
RULE FILE TITLE:      [REDACTED]  
LVS MODE:             Mask  
RULE FILE NAME:        [REDACTED]  
CREATION TIME:         Fri Oct 23 11:12:36 2009  
CURRENT DIRECTORY:    [REDACTED]  
USER NAME:             [REDACTED]  
CALIBRE VERSION:      v2009.2_36.21   Tue Jul 7 15:59:45 PDT 2009
```

OVERALL COMPARISON RESULTS

```
      #          #####  
      #          #          #          - -  
      # #        #   CORRECT #          * *  
      # #        #          #          |  
      #          #          #          \__/  
      #          #####
```

NUMBERS OF OBJECTS

	Layout	Source	Component Type
	-----	-----	-----
Ports:	4	4	
Nets:	4	4	
Instances:	1	1	MN (4 pins)
	1	1	MP (4 pins)
	-----	-----	
Total Inst:	2	2	

LVS PARAMETERS

o LVS Setup:

```
// LVS COMPONENT TYPE PROPERTY
// LVS COMPONENT SUBTYPE PROPERTY
// LVS PIN NAME PROPERTY
LVS POWER NAME           "VCC" "VDD" "VDD3V" "DVDD" "V3IO" "V2IO"
LVS GROUND NAME          "VSS" "GND" "VBB" "VOIO" "DGND"
LVS CELL SUPPLY          NO
LVS RECOGNIZE GATES      ALL
LVS IGNORE PORTS         NO
LVS CHECK PORT NAMES     NO
LVS IGNORE TRIVIAL NAMED PORTS NO
LVS BUILTIN DEVICE PIN SWAP YES
LVS ALL CAPACITOR PINS SWAPPABLE NO
```

LVS DISCARD PINS BY DEVICE	NO
LVS SOFT SUBSTRATE PINS	NO
LVS INJECT LOGIC	YES
LVS EXPAND UNBALANCED CELLS	YES
LVS EXPAND SEED PROMOTIONS	NO
LVS PRESERVE PARAMETERIZED CELLS	NO
LVS GLOBALS ARE PORTS	YES
LVS REVERSE WL	NO
LVS SPICE PREFER PINS	NO
LVS SPICE SLASH IS SPACE	YES
LVS SPICE ALLOW FLOATING PINS	YES
// LVS SPICE ALLOW INLINE PARAMETERS	
LVS SPICE ALLOW UNQUOTED STRINGS	NO
LVS SPICE CONDITIONAL LDD	NO
LVS SPICE CULL PRIMITIVE SUBCIRCUITS	NO
LVS SPICE IMPLIED MOS AREA	NO
// LVS SPICE MULTIPLIER NAME	
LVS SPICE OVERRIDE GLOBALS	NO
LVS SPICE REDEFINE PARAM	NO
LVS SPICE REPLICATE DEVICES	NO
LVS SPICE SCALE X PARAMETERS	NO
LVS SPICE STRICT WL	NO
// LVS SPICE OPTION	
LVS STRICT SUBTYPES	NO
LVS EXACT SUBTYPES	NO
LAYOUT CASE	NO
SOURCE CASE	NO
LVS COMPARE CASE	NO
LVS DOWNCASE DEVICE	NO
LVS REPORT MAXIMUM	50
LVS PROPERTY RESOLUTION MAXIMUM	32
// LVS SIGNATURE MAXIMUM	
LVS FILTER UNUSED OPTION	AB RC RE RG
// LVS REPORT OPTION	
LVS REPORT UNITS	YES
// LVS NON USER NAME PORT	
// LVS NON USER NAME NET	
// LVS NON USER NAME INSTANCE	

Nets:	4	4	0	0	
Instances:	1	1	0	0	MN(N_18)
	1	1	0	0	MP(P_18)
	-----	-----	-----	-----	
Total Inst:	2	2	0	0	

o Initial Correspondence Points:

Ports: vi vdd! gnd! vo

SUMMARY

Total CPU Time: 0 sec

Total Elapsed Time: 1 sec

